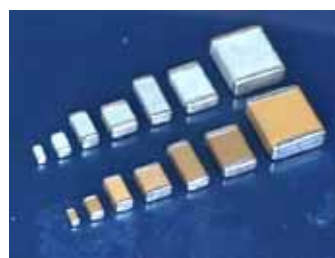


## Multilayer Ceramic Chip Capacitors [ High Voltage Capacitor ]

### HVC Series – Middle Voltage (100V-1KV)



Voltage ratings from 100V to 1KV and sizes from 0603 to 2225.

#### ◆ Features

- ❑ Special internal electrode design offers the highest voltage rating
- ❑ Surface mount suited for wave and reflow soldering
- ❑ High reliability
- ❑ RoHS compliant

#### ◆ Applications

- ❑ Suitable for LAN/WLAN interface, Back-Lighting Inverter, DC-DC Converters, Ballast, Modems & Power Supplies.

#### ◆ Summary of Specification

Operation Temperature	-55~+125
Rated Voltage	100Vdc to 1000Vdc
Temperature Coefficient	NPO : $\leq \pm 30\text{ppm/}$ , -55~+125 (EIA Class )
	X7R : $\leq \pm 15\%$ , -55~+125 (EIA Class )
Capacitance Range	NPO :2pF to 100nF ; X7R :150pF to 2.2uF
Dissipation Factor :	NPO : Q 1000 ; X7R : D.F. 2.5%
Insulation Resistance	10G $\Omega$ or 500/C $\Omega$ whichever is smaller
Aging	NPO:0% ; X7R: 2.5 % per decade of time
Dielectric Strength	200V V < 500V : 200% Rated Voltage
	500V V < 1000V : 150% Rated Voltage
	1000V V : 120% Rated Voltage

#### ◆ How To Order



Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Special Requirement
C: MLCC (Multilayer Ceramic Chip of Capacitor)	Ex.: 1206 : 3.2×1.6mm 1808 : 4.6×2.0mm 1812: 4.6×3.2mm	Ex.: N: NPO X: X7R	Ex.: 2R0:2.0pF 100:10×10 <sup>0</sup> 471:47×10 <sup>1</sup> 102:10×10 <sup>2</sup>	Ex.: C:±/-0.25pF D:±/-0.50pF J :±/- 5% K :±/-10% M:±/-20%	Ex.: 251:250Vdc 631:630Vdc 102:1000Vdc	T: Taping & Reel B: Bulk	Ex.: O: Arc Prevention Coating X: Cushion Termination (Super Term)





## Multilayer Ceramic Chip Capacitors [ High Voltage Capacitor ]

### HVC Series – High Voltage Capacitors (2KV)



This series is offered in C0G(NPO) and X7R temperature characteristic in sizes from 1206 to 2220.

#### ◆ Features

- Standard for telecommunication devices (IEEE802.3) in LAN interface.
- Available with proprietary coating to prevent arcing and pass Hi-Pot test
- Surface mount suited for wave and reflow soldering
- Special internal electrode design offers the highest voltage rating
- RoHS compliant

#### ◆ Applications

- Suitable for telecommunication devices in LAN interface
- Ballast capacitor for back light inverter applications

#### ◆ Summary of Specification

Operation Temperature	-55~+125
Rated Voltage	2KVdc
Temperature Coefficient	NPO : $\leq \pm 30\text{ppm/}$ , -55~+125 (EIA Class )
	X7R : $\leq \pm 15\%$ , -55~+125 (EIA Class )
Capacitance Range	NPO : 2pF to 470pF ; X7R : 150pF to 10nF
Dissipation Factor :	NPO : Q 1000 ; X7R : D.F. 2.5%
Insulation Resistance	10G $\Omega$ or 500/C $\Omega$ whichever is smaller
Aging	NPO:0% ; X7R: 2.5 % per decade of time
Dielectric Strength	120% Rated Voltage

#### ◆ How To Order

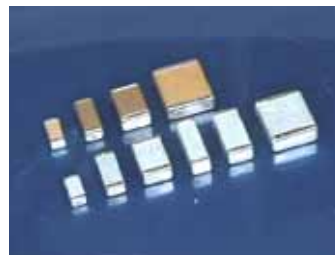


Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Special Requirement
C: MLCC (Multilayer Ceramic Chip of Capacitor)	Ex.: 1206 : 3.2×1.6mm 1808 : 4.6×2.0mm 1812: 4.6×3.2mm	Ex.: N: NPO X: X7R	Ex.: 2R0:2.0pF 100:10×10 <sup>0</sup> 471:47×10 <sup>1</sup> 102:10×10 <sup>2</sup>	Ex.: C: +/-0.25pF D: +/-0.50pF J : +/- 5% K : +/-10% M: +/-20%	Ex.: 202:2000Vdc	T: Taping & Reel B: Bulk	Ex.: O: Arc Prevention Coating X: Cushion Termination (Super Term)



## Multilayer Ceramic Chip Capacitors [ High Voltage Capacitor ]

### HVC Series – Ultra High Voltage (3KV-5KV)



Capacitors with voltage ratings or 3KV to 5KV are ideally suited for LCD back light inverter circuits.

#### ◆ Features

- Super term termination prevents cracking solution mechanical stress
- Special internal electrode design offers the highest voltage rating
- Surface mount suited for wave and reflow soldering
- High reliability
- RoHS compliant

#### ◆ Applications

- Suitable for back-lighting inverter ,DC-DC converters, modems and power supplies

#### ◆ Summary of Specification

Operation Temperature	-55~+125
Rated Voltage	3KVdc to 5KVdc
Temperature Coefficient	NPO : $\leq \pm 30\text{ppm/}$ , -55~+125 (EIA Class )
	X7R : $\leq \pm 15\%$ , -55~+125 (EIA Class )
Capacitance Range	NPO :2pF to 1.0nF ; X7R :150pF to 2.7nF
Dissipation Factor :	NPO : Q 1000 ; X7R : D.F. 2.5%
Insulation Resistance	10G $\Omega$ or 500/C $\Omega$ whichever is smaller
Aging	NPO:0% ; X7R: 2.5 % per decade of time
Dielectric Strength	120% Rated Voltage

#### ◆ How To Order

C

1808

N

270

K

302

T

X

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Special Requirement
C: MLCC (Multilayer Ceramic Chip of Capacitor)	Ex.: 1206 : 3.2×1.6mm 1808 : 4.6×2.0mm 1812: 4.6×3.2mm	Ex.: N: NPO X: X7R	Ex.: 2R0:2.0pF 100:10×10 <sup>0</sup> 471:47×10 <sup>1</sup> 102:10×10 <sup>2</sup>	Ex.: C: +/-0.25pF D: +/-0.50pF J : +/- 5% K : +/-10% M: +/-20%	Ex.: 302:3000Vdc 502:5000Vdc	T: Taping & Reel B: Bulk	Ex.: O: Arc Prevention Coating X: Cushion Termination (Super Term)

