

# NPN RF Transistor MMBT5179

# **Description**

This device is designed for use in low noise UHF/VHF amplifiers with collector currents in the  $100~\mu A$  to 30~mA range in common emitter or common base mode of operation, and in low frequency drift, high ouput UHF oscillators. Sourced from Process 40.

#### **Features**

• This Devices is Pb–Free, Halogen Free/BFR Free and is RoHS Compliant

#### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	12	V
V <sub>CBO</sub>	Collector-Base Voltage	20	V
V <sub>EBO</sub>	Emitter-Base Voltage	2.5	V
I <sub>C</sub>	Collector Current - Continuous	50	mA
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range (Note 1)	-55 to + 150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

# THERMAL CHARACTERISTICS

(T<sub>A</sub> = 25°C unless otherwise noted) (Note 3)

Symbol	Characteristic	Max	Unit
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	225 1.8	MW mW/°C
RθJA	Thermal Resistance, Junction to Ambient	556	°C/W

- 1. These ratings are based on a maximum junction temperature of 150°C.
- These are steady-state limits. onsemi should be consulted on applications involving pulsed or low-duty cycle operations.
- 3. Device mounted on FR-4 PCB 1.6"  $\times$  1.6"  $\times$  0.06".



1. Base 2. Emitter 3. Collector

SOT-23 CASE 318-08

#### MARKING DIAGRAM



3C = Specific Device Code

M = Date Code\*

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMBT5179	SOT-23 (Pb-Free)	3000 / Tape and Real

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

#### **MMBT5179**

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Max	Unit
V <sub>CEO(sus)</sub>	Collector-Emitter Sustaining Voltage (Note 4)	I <sub>C</sub> = 3.0 mA, I <sub>B</sub> = 0	12		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 1.0 \mu\text{A},  I_E = 0$	20		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 10 μA, I <sub>C</sub> = 0	2.5		V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = 15 V, I <sub>E</sub> = 0		0.02	μΑ
		V <sub>CB</sub> = 15 V, T <sub>A</sub> = 150°C		1.0	μΑ

#### **ON CHARACTERISTICS**

h <sub>FE</sub>	DC Current Gain	$I_C = 3.0 \text{ mA}, V_{CE} = 1.0 \text{ V}$	25	250	
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1.0 mA		0.4	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 1.0 mA		1.0	V

#### **SMALL SIGNAL CHARACTERISTICS**

f <sub>T</sub>	Current Gain - Bandwidth Product	$I_C = 5.0 \text{ mA}, V_{CE} = 6.0 \text{ V}, f = 100 \text{ MHz}$	900	2000	MHz
C <sub>cb</sub>	Collector-Base Capacitance	$V_{CB} = 10 \text{ V}, I_E = 0, f = 0.1 \text{ to } 1.0 \text{ MHz}$		1.0	pF
h <sub>fe</sub>	Small-Signal Current Gain	$I_C = 2.0 \text{ mA}, V_{CE} = 6 \text{ V}, f = 1.0 \text{ kHz}$	25	300	
rb'C <sub>c</sub>	Collector Base Time Constant	$I_C = 2.0 \text{ mA}, V_{CB} = 6.0 \text{ V}, f = 31.9 \text{ MHz}$	3.0	14	ps
NF	Noise Figure	$I_C$ = 1.5 mA, $V_{CE}$ = 6.0 V, $R_S$ = 50 $\Omega$ , f = 200 MHz		5.0	dB

#### **FUNCTIONAL TEST**

G <sub>pe</sub>	Amplifier Power Gain	$V_{CE} = 6.0 \text{ V}, I_{C} = 5.0 \text{ mA}, f = 200 \text{ MHz}$	15	dB
Po	Power Output	$V_{CB}$ = 10 V, $I_E$ = 12 mA, $f \ge 500$ MHz	20	mW

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

# **SPICE MODEL**

NPN (Is=69.28E-18 Xti=3 Eg=1.11 Vaf=100 Bf=282.1 Ne=1.177 Ise=69.28E-18 Ikf=22.03m Xtb=1.5 Br=1.176 Nc=2 Isc=0 Ikr=0 Rc=4 Cjc=1.042p Mjc=.2468 Vjc=.75 Fc=.5 Cje=1.52p Mje=.3223 Vje=.75 Tr=1.588n Tf=135.6p Itf=.27 Vtf=10 Xtf=30 Rb=10)

<sup>4.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

#### **MMBT5179**

#### **TYPICAL CHARACTERISTICS**

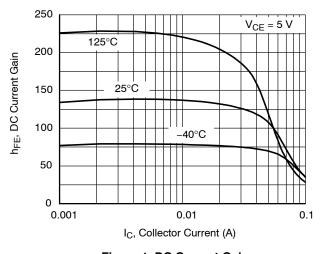


Figure 1. DC Current Gain vs. Collector Current

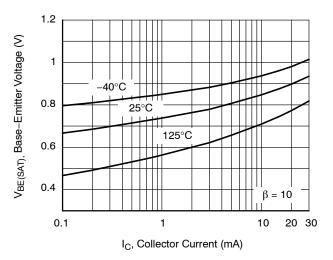


Figure 3. Base-Emitter Saturation Voltage vs. Collector Current

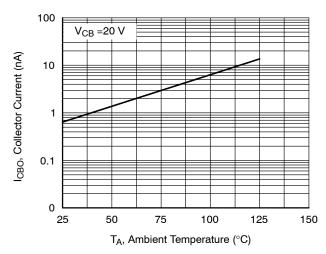


Figure 5. Collector Cut-Off Current vs. Ambient Temperature

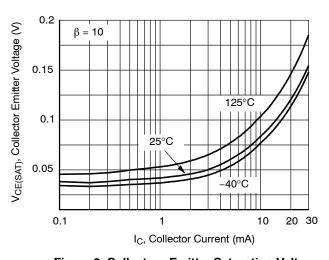


Figure 2. Collector – Emitter Saturation Voltage vs. Collector Current

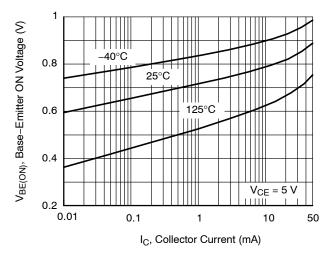


Figure 4. Base–Emitter ON Voltage vs. Collector Current

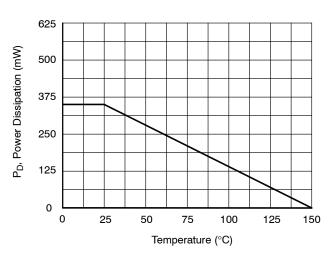


Figure 6. Power Dissipation vs. Ambient Temperature

# **MMBT5179**

# **TEST CIRCUIT**

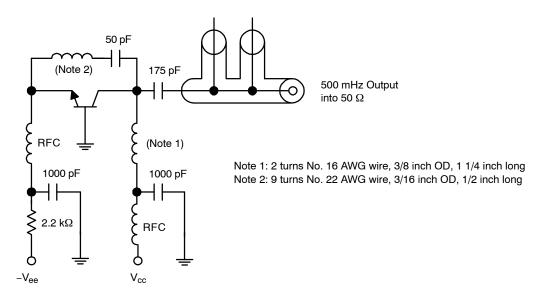


Figure 7. 500 MHz Oscillator Circuit

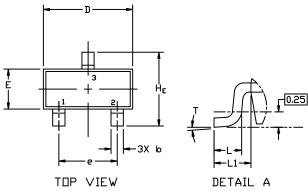




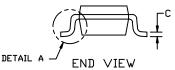
**SOT-23 (TO-236)** CASE 318 ISSUE AT

**DATE 01 MAR 2023** 









#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
Ε	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0*		10°	0*		10*



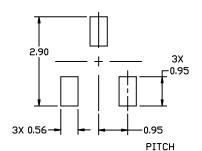


XXX = Specific Device Code

M = Date Code

■ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

# **STYLES ON PAGE 2**

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# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



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**DATE 01 MAR 2023** 

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE	ı	
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE		PIN 1. SOURCE	PIN 1. CATHODE
2. ANODE	2. SOURCE	2. CATHODE		2. DRAIN	2. GATE
3. CATHODE	3. GATE	3. CATHODE-ANODE		3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	I PIN 1. CATHODE	PIN 1. CATHODE
2. CATHODE	2. CATHODE	2. ANODE	2. CATHODE	2. ANODE	2. ANODE
3. ANODE	3. CATHODE	3. CATHODE	3. ANODE	3. CATHODE-ANODE	3. GATE
STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
2. SOURCE	2. OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3. DRAIN	3. INPUT	3. CATHODE	3. SOURCE	3. GATE	3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

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