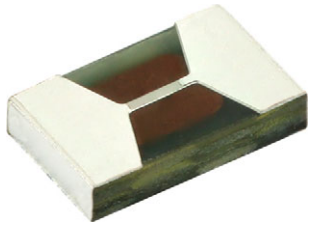


# Massive Electro-Pyrotechnic Initiator Chip Resistor



## LINKS TO ADDITIONAL RESOURCES



The Massive Electro-Pyrotechnic Initiator Chips (MEPIC) are resistors dedicated to pyrotechnic applications. The MEPIC resistors are the Surface Mount Device (SMD) variants from the established NiCr on FR type substrate technology from Vishay Sfernice. The standard case geometry (SMD) enables the implementation of assembly process commonly used in the electronic component industry (pick and place, reflow soldering on flat PCB used as header) providing high productivity. The principle of MEPIC is to convert electrical energy into heat in a precise electro-thermal profile for the purpose of initiating a series of pyrotechnic events in a controlled energetic reaction. In the mining industry this effect is commonly used for the ignition of electronic detonators (digital blasting). Other industries such as firework (e-match manufacturing) and demolition (various electric detonators) are also focused applications.

The MEPIC design has been developed specifically to offer an alternative to the Bridge Wire (BW) technology, insuring at least the same level of performances, while providing cost efficient assembly process and initiator design alternatives.

## TECHNOLOGY

The MEPIC active area (heating zone) will be impregnated by the user with a primary pyrotechnic material (usually wet primer followed by drying) in such way to ensure an intimate contact for an optimum heat transfer of thermal energy. The geometry of the active area of the MEPIC, and both the primer chemistry and its impregnation method, will determine the global performances. Note that the active area of MEPIC shall not be put in direct contact with explosive powder as grain size will not ensure intimate contact and will induce non reproducible and non reliable performances.

The two main characteristics of a MEPIC resistor are their “All Fire” (AF) and “No Fire” (NF) performances:

- “All Fire” (AF) represents the command pulse where the major amount of the dissipated energy will be transferred to the primer to generate the ignition. Customer will have to provide Vishay Sfernice with “All Fire” conditions, usually with capacitance discharge parameters or with Minimum Current or Voltage and corresponding short pulse duration.
- “No Fire” (NF) represents the immunity of the resistor with primer to the environmental electro-magnetic pollution and electric continuity test, where the major amount of the dissipated energy will be transferred to the substrate to ensure no ignition. Customer will have to provide Vishay Sfernice with “No Fire” conditions, usually maximum current or voltage and corresponding longest duration. In case of applicable capacitance discharge test the parameters shall also be provided.

## ASSEMBLY PRECAUTIONS

In order to obtain reproducible ignition performances it is important that the assembly process fulfills the following criteria:

- Do not use iron soldering method to mount the MEPIC on its header because uncontrolled amount of solder could impact the heat transfer (potential misfire or ignition delay) and local over heating may damage the MEPIC (deformation that may cause active area cracks).
- Take specific precautions, such as no air bubble during preparation and application of primer, in order to ensure the intimate contact of pyrotechnic primer and MEPIC active area (potential misfire).
- Take specific handling precaution in order not to damage MEPIC active area (ex: pickup head design for pick and place or specific fixing tools in the entire assembly process).
- All along the assembly process, take specific care to extreme thermo-mechanic stress that could be applied to the MEPIC (such as stress induced during over molding) because the active area of MEPIC is subjected to crack (and generate unstable resistance value).
- The MEPIC reliability is only guaranteed for one single reflow profile.
- In case of necessity to dismantle a MEPIC, another MEPIC must be used (no rework is allowed).
- Pay specific attention to the cleaning process after reflow soldering in order not to damage the active area and to keep it clean from various pollutions.

## FEATURES

- Surface mount design for standard assembly process
- SMD version only
- Active area designed upon performances
- Case size 0805
- Firing energy down to 1.0 mJ <sup>(1)</sup>
- Firing time down to 250 μs
- Ohmic value: 2 Ω to 8 Ω ± 10 % (typical) <sup>(2)</sup>
- Joule effect ignition
- Easy set up by design of firing levels
- Very predictable, reproducible and reliable behavior
- Compatibility with pyrotechnic element has to be tested in real environment
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



## Notes

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

<sup>(1)</sup> Ignitor performances are dependent on both pyrotechnic primer chemistry and active areas geometry

<sup>(2)</sup> For ohmic value < 3 Ω the tolerance will be discussed with Vishay Sfernice

<b>STANDARD ELECTRICAL SPECIFICATIONS</b>			
MODEL	SIZE / CASE DESIGNATION <sup>(1)</sup>	RESISTANCE RANGE $\Omega$	RESISTANCE TOLERANCE %
MEPIC (SMD)	0805	2 to 8	10 to 30

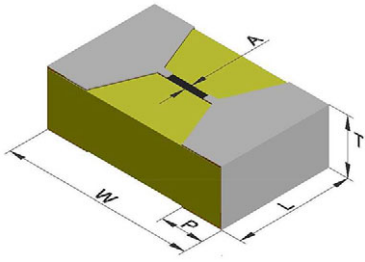
**Note**

<sup>(1)</sup> Detailed dimensions are specified in Dimensions and Tolerances table

<b>RANGE OF IGNITION PERFORMANCES</b>					
MODEL	“NO FIRE” CURRENT A	“NO FIRE” DURATION s	“ALL FIRE” CURRENT A	IGNITION TIME ms	“ALL FIRE” ENERGY $\mu$ J
MEPIC (SMD)	0.5 to 1.2	2 to 10	Down to 1	Down to 0.25	Down to 1000

**Note**

• Ignition performances are dependent on both pyrotechnic primer chemistry and active area geometry

<b>DIMENSIONS AND TOLERANCES</b> in millimeters (inches)							
							
MODEL	SIZE / CASE DESIGNATION	WIDTH W	LENGTH L	THICKNESS T	INSULATION DISTANCE I	BACK SIDE PADS P	ACTIVE AREA WIDTH TOLERANCE A
MEPIC (SMD)	0805	$2.00 \pm 0.15$ ( $0.080 \pm 0.006$ )	$1.25 \pm 0.10$ ( $0.050 \pm 0.004$ )	$0.6 \pm 0.1$ ( $0.024 \pm 0.004$ )	-	$0.50 \pm 0.15$ ( $0.020 \pm 0.006$ )	$\pm 0.01$ ( $\pm 0.0004$ )

**CONSTRUCTION**

- Substrate: epoxy based (FRx type)
- Resistive element: NiCr
- Terminations: SMD wraparound
- Tin plated copper

**HOW TO GET THE RIGHT MEPIC**

Each MEPIC will have to be adapted to customer pyrotechnic primer chemistry (energetic material). To reach the right MEPIC design it is necessary to work by “iterations”. Upon receipt of the MEPIC Design Guide duly filled, an initial sampling lot is given to customer (along with a MEPIC reference) so he can provide “No Firing” / “All Firing” performances obtained after first testing. After the analysis of these first test results a new set of samples will be proposed (eventually tooling charges will be necessary) in order to get closer to the customer requirements. It may be several iterations until the right design is found. It may also happen that all requirements cannot be fulfilled simultaneously and then a compromise will be necessary between MEPIC design and customer pyrotechnic primer chemistry or ignition parameters.

When the iterations are finished, which means that the design is validated with total or partial requirements fulfilled, Vishay Sfernice will design a final set of photomasks for serial production.



**ORDERING PROCEDURE**

M	E	P	I	C	8	5	H	2	R	0	I	T	T	X	X
MODEL	SIZE	BREAKDOWN ENERGY CLASS (mJ)					VALUE (R)	TOLERANCE (%)	TERMINATION MATERIAL <sup>(1)</sup>	PACKAGING <sup>(2)</sup>			OPTION		
MEPIC	85 = 0805	<b>H</b> = 1 ± 0.3 <b>M</b> = 3 ± 0.3 <b>D</b> = 7 ± 1 <b>N</b> = 10 ± 1.5 <b>E</b> = 15 ± 1.5 <b>V</b> = 20 ± 2 <b>T</b> = 30 ± 2 <b>Q</b> = 40 ± 2					1R0 1R5 2R0 2R8 3R7 4R0 6R0 8R0	<b>J</b> = 5 <b>I</b> = 7.5 <b>K</b> = 10 <b>D</b> = 12.5 <b>Q</b> = 15 <b>L</b> = 20 <b>M</b> = 25 <b>N</b> = 30	<b>T</b> = tin  Other options possible  Please consult	<b>W</b> = waffle pack <b>T</b> = tape and reel (plastic tape) <b>B</b> = bag			...		

**Historical Part Number Examples:**

MEPIC8501W ⇒ MEPIC85H2R0ITW	MEPIC8850WTT ⇒ MEPIC85M1R0LTT
MEPIC8805W ⇒ MEPIC85M2R0ITW	MEPIC8823WTT ⇒ MEPIC85M2R0KTT
MEPIC8805WTT ⇒ MEPIC85M2R0ITT	MEPIC8005WTT ⇒ MEPIC85M2R0ITT
MEPIC8022WTT ⇒ MEPIC85N8R0KTT	MEPIC8860WTT ⇒ MEPIC85N0R8MTT
MEPIC8848WTT ⇒ MEPIC85N2R0KTT	MEPIC8501WTT ⇒ MEPIC85H2R0ITT
MEPIC8858WTT ⇒ MEPIC85N2R0KTT	

**Notes**

- MEPIC being a semi-custom product, please fill EPIC / MEPIC Design Guide ([www.vishay.com/doc?53045](http://www.vishay.com/doc?53045)) and send to [sferthinfilm@vishay.com](mailto:sferthinfilm@vishay.com) to get appropriate part number
- (1) Tin termination finish only valid for reflow soldering
- (2) Customer assembly process requirement:
  - Waffle pack for manual placing on PCB
  - Tape and reel for automatic pick and place
  - Bag for bowl feeding



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